

APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date:: 01-29-2004
Application Type:: Regular
Subject Matter:: Utility
Title:: Low Jitter Digital Frequency
Synthesizer and Control Thereof
Attorney Docket Number:: X-1528 US
Request for Early Pub?:: No
Request for Non-Pub?:: Yes
Total Drawing Sheets:: 12
Small Entity?:: No
Petition included?:: No

APPLICANT INFORMATION

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: John
Middle Name:: D.
Family Name:: Logue
Street:: 3290 Serenity Drive
City:: Placerville
State or Province:: California
Postal or Zip Code:: 95567

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Austin
Middle Name:: H.
Family Name:: Lesea
Street:: 25542 Mt. Bache Road
City:: Los Gatos

State or Province:: California
Postal or Zip Code:: 95033

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: P.R. China
Status:: Full Capacity
Given Name:: Wei
Family Name:: Lu
Street:: 3043 Vin Grande Court
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95135

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

Representative Customer Number::	24309	
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ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.
Street:: 2100 Logic Drive
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95124